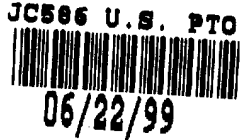


Customer No. 20350

TOWNSEND and TOWNSEND and CREW LLP
Two Embarcadero Center, 8th Floor
San Francisco, California 94111-3834
(415) 576-0200

ASSISTANT COMMISSIONER FOR PATENTS
BO^Y PATENT APPLICATION
Washington, D.C. 20231



Attorney Docket No. 7722-298US

"Express Mail" Label No. EL104833135US

Date of Deposit: June 22, 1999

I hereby certify that this is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above, addressed to:

Assistant Commissioner for Patents
Washington, D.C. 20231

By: 

Sir:

Transmitted herewith for filing is the
[X] patent application of

Inventor(s)/Applicant Identifier: Richard Snow and Steven Shaffer

For: PROCESSING PLATFORM FOR A GAMING MACHINE

Enclosed are:

[X] 2 sheet(s) of [] formal [X] informal drawing(s).
[X] A [] signed [X] unsigned Declaration.

In view of the Unsigned Declaration as filed with this application and pursuant to 37 CFR §1.53(d),
Applicant requests deferral of the filing fee until submission of the Missing Parts of Application.

DO NOT CHARGE THE FILING FEE AT THIS TIME.

Kevin T. LeMond
Reg No.: 35, 933
Attorneys for Applicant

Telephone:
(415) 576-0200

Facsimile:
(415) 576-0300



PATENT APPLICATION

PROCESSING PLATFORM FOR A GAMING MACHINE

Inventor(s):

Richard Snow
4725 Travis Way
Reno, NV 89502
a citizen of United States of America

Steven Shaffer
15508 Quicksilver Drive
Reno, NV 89511
a citizen of United States of America

Assignee:

International Game Technology
9295 Prototype Drive
Reno, Nevada 89511

Entity: Large

TOWNSEND and TOWNSEND and CREW LLP
Two Embarcadero Center, 8th Floor
San Francisco, California 94111-3834
(415) 576-0200

PROCESSING PLATFORM FOR A GAMING MACHINE

5

BACKGROUND OF THE INVENTION

1. Field Of The Invention

10 The present invention relates to a processing platform for operation of a gaming machine, and more particularly, to a processing platform for operation of a gaming machine that includes a general computing subsystem and a gaming processing subsystem.

2. Description Of The Prior Art

15 Slot machine designs are growing in complexity. Regulation and control of modern gaming devices has also correspondingly become complex. Thus, protecting the public from improper machine operation has become a daunting task. Regulation aimed at close control of slot machine design has resulted in greater machine design diversity thereby compounding the complexity of machine control.

20 Various bodies of gaming law have a variety of technical and legal requirements. These requirements impact slot machine designs in many ways. Security, randomness, payback percentage, game play history, code verification and accounting for all facets of game operation are commonly regulated operational characteristics of slot machines. These regulations often limit slot machine designers. To meet technical requirements, engineers are often faced with complex operations. These operations delay
25 machine start up, lead to complex human attendant operations, and increase opportunity for error.

30 In current gaming machines, processing platforms are adopted to allow various player features while also supporting features required by various gaming regulatory bodies. As computer and computer software advancements are made, gaming machine manufacturers constantly update designs for complete gaming platforms that require extensive engineering effort and time consuming regulatory submittal and testing. The constant re-design and engineering results in diverse implementations of regulated

machine functions that are fundamental elements of any gaming device. This constant re-design and engineering results in great expense. The diversity of these implementations causes increased complexity for operators and regulatory agencies.

5

SUMMARY OF THE INVENTION

A processing platform for operation of a gaming machine in accordance with the present invention includes a bus, a gaming processing subsystem for controlling aspects of gaming machine operation that involve game functionality and thus are generally subject to regulation coupled to the bus and a general computing subsystem for
10 controlling aspects of gaming machine operation that do not involve game functionality and thus are not generally subject to regulation. The general computing subsystem is also coupled to the bus and the gaming processing subsystem is physically separate from the general computing subsystem.

In accordance with one aspect of the present invention, the bus uses an
15 interface protocol that consists of one of PCI, ISA, VME and AGP.

In accordance with another aspect of the present invention, the aspects of gaming machine operation that involve game functionality and are more likely to be the subject of regulation include game play history, game accounting, gaming machine access, I/O control, random number generation and game authentication algorithms.

20 In accordance with a further aspect of the present invention, the aspects of gaming machine operation that do not involve game functionality and are less likely to be the subject of regulation include player visual display and attract animation, audio player feedback and attraction, real time video presentations, and commercial operating systems.

Accordingly, the processing platform for operation of a gaming machine in
25 accordance with the present invention allows for the processing to be performed by two physically separate subsystems, one directed to the actual gaming processing and the other directed to general computing processing. Thus, gaming regulatory agencies will be able to regulate and approve the gaming processing subsystem with regard to, for example, functional aspects of gaming related to game play history, gaming accounting,
30 gaming machine access, input/output control, random number generation, game authentication algorithms, and general issues related to gaming and money, such as pay tables, that are necessary for protecting players, i.e., consumers. At the same time, gaming machine manufacturers are able to improve and upgrade gaming machines with

ever improving hardware and software for better graphics and sound related to playing the game, graphics and sound for attracting players to the game, and other aspects generally related to non-functional entertainment features with the gaming machines. Hence, once a regulatory agency approves a gaming processing subsystem, that game processing subsystem may be used with general computing subsystems to control and operate gaming machines. Accordingly, gaming machines can quickly and easily be upgraded, changed, and developed with a reduced need for gaming regulatory agency testing and approval.

BRIEF DESCRIPTION OF THE DRAWINGS

Figure 1 is a schematic illustration of a processing platform for operation of a gaming machine in accordance with the present invention; and

Figure 2 is a schematic illustration of a gaming machine including a processing platform for operation thereof in accordance with the present invention.

DETAILED DESCRIPTION OF THE PREFERRED EXEMPLARY EMBODIMENTS

A processing platform 10 for operation of a gaming machine is schematically illustrated in Figure 1. The processing platform is preferably subdivided into two separate primary subsystems 11, 12 coupled to bus 13. General computing platform 11 controls software and hardware needed to support display, sound and other non-gaming critical functions while gaming processing platform 12 controls primary gaming operations.

Preferably, the general computing platform or subsystem consists of a common PC-type personal computer and therefore preferably consists of a PCI type expansion card that includes bus interface 14. The general computing platform may consist of multiple cards if desired or needed. Preferably, general computing platform 11 communicates with gaming processing platform 12 with a series of software driven API-type calls. The general computing subsystem preferably controls or operates, for example, player visual displays and attraction animation features of the gaming machine; audio player feedback in player attraction features of the gaming machine; real time video presentations; and any commercial operating system present within the gaming machine.

Gaming processing platform or subsystem 12 is preferably also constructed on a standard PCI type expansion card that includes bus interface 15.

Preferably, the gaming processing platform consists of a single card, but may consist of multiple cards if desired. This peripheral component interface serves as the controller of all gaming specific operations. These gaming operations preferably include, for example, game play history, i.e., what is played, what has been displayed, etc.; game accounting, i.e., money coming in and out; gaming machine access, i.e., doors opening on the gaming machine, when the door was opened, etc.; input/output control, i.e., user interface, such as buttons, handles, coin acceptors; random number generation; and game authentication algorithms. Accordingly, it should be apparent that gaming processing platform 12 generally controls gambling or gaming aspects associated with the gaming machine or system that are typically important to the integrity and security of the game, and thus generally are subject to regulation by gaming authorities or gaming regulation agencies.

The gaming processing subsystem board preferably consists of a PCI half-length card. This card may be fitted to any processor architecture that supports PCI. The gaming processing card will have a processor 20, which will control the gaming machine I/O including communications. Additionally, the gaming processing card preferably will include all gaming random number generation functionality, data table information that pertains to payout information stored in a non-volatile memory area 21. This design of a gaming processing platform allows any PC or other processor architecture to be quickly adopted as a gaming machine platform and minimizes any custom or in-house non-standard work, such as software and hardware relating to improved graphics and sounds for general computing operations. Because all game outcome and paying information is generated, backed-up and controlled on the gaming processing platform, a reduction in the number of gaming approvals by gaming regulatory agencies may be achieved.

The gaming processing board also preferably includes a data memory socket 22 that accommodates existing data proms from older gaming devices, such as S-Plus machines (SSPROMs) and PE-Plus machines (XP type data proms), both manufactured by International Game Technology. Since all communications are coupled to hardware on the gaming processing board, money devices, such as bill validators, are ultimately controlled by the gaming processing board.

Preferably, the gaming processing board further includes serial UARTS 23 that are part of the input/output control. Serial UARTS communicate with the various games and communicate various information in and out of the game. Furthermore, the gaming processing board also includes the controls 24 for communications external of the

gaming machine, for example, information relating to large progressive jackpots among a general type of machine, i.e., when the machines are part of a larger gaming subsystem, and information relating to the amount of coins in, coins out and wins being fed to a central computing location.

Figure 2 illustrates a gaming machine 30 that includes a housing 31, at least one user input 32 coupled to the housing, a display 33, such as, for example, a CRT, LCD or plasma display, coupled to the housing, a money input 34 (for example, coin slot, bill validator, coupon acceptor, smart card reader, credit/debit card reader, or other devices for accepting currency or credit), and a money output 35 (for example, coin chute, ticket printer, smart card writer, or other devices for issuing currency or credit). The gaming machine includes a control system 34 that includes processing platform 10 consisting of gaming processing subsystem 12 and the general computing subsystem 11.

Accordingly, the present invention provides a processing platform for operation of a gaming machine and allows for the creation and approval of various gaming processing subsystems or boards that can be coupled with general computing subsystems or boards that control features of the gaming machine operation that are not essential to regulatory aspects of the device. Therefore, these general computing subsystems or boards may be continually and easily changed and upgraded. This allows for rapid and easy development of gaming machines to continually upgrade the appeal and entertainment factors related to gaming machines. Furthermore, a processing platform for operation of a gaming machine in accordance with the present invention allows for better security with respect to gaming aspects while allowing a completely open architecture gaming platform.

The present invention has been described using the standard PCI computing interface. However, those skilled in the art will recognize that other standard computing interfaces, such as, for example, ISA, VME, and AGP may also be employed. Furthermore, the present invention has been described with respect to being contained within a gaming machine. Those skilled in the art will recognize that any or all of the processing platform may be external to the gaming machine such as, for example, in the case wherein a central control system exists for controlling one or more gaming machines.

Although the invention has been described with reference to specific exemplary embodiments, it will be appreciated that it is intended to cover all modifications and equivalents within the scope of the appended claims.

WHAT IS CLAIMED IS:

1 1. A processing platform for operation of a gaming machine, the
 2 processing platform comprising:
 3 a bus;
 4 a gaming processing subsystem coupled to the bus for controlling
 5 functional aspects of gaming machine operation that involve game functionality; and
 6 a general computing subsystem coupled to the bus for controlling
 7 non-functional aspects of gaming machine operation that do not involve game
 8 functionality;
 9 wherein the gaming processing subsystem is physically separate
 10 from the general computing subsystem.

1 2. A processing platform in accordance with claim 1 wherein the bus
 2 uses an interface protect that consists of one of PCI, ISA, VME, and AGP.

1 3. A processing platform in accordance with claim 1 wherein the
 2 functional aspects of gaming machine operation comprise at least one of game play
 3 history, game accounting, gaming machine access, I/O control, random number
 4 generation and game authentication algorithms.

1 4. A processing platform in accordance with claim 1 wherein the non-
 2 functional aspects of gaming machine operation comprise at least one of player visual
 3 display and attract animation, audio player feedback and attraction, real time video
 4 presentations, and commercial operating systems.

1 5. A processing platform for operation of a gaming machine, the
 2 processing platform comprising:
 3 a bus that uses an interface protocol consisting of one of PCI, ISA,
 4 VME, and AGP;
 5 a gaming processing subsystem coupled to the bus for controlling
 6 functional aspects of gaming machine operation, the functional aspects of gaming
 7 machine operation comprising at least one of game play history, game accounting,

gaming machine access, I/O control, random number generation and game authentication algorithms; and

a general computing subsystem coupled to the bus for controlling non-functional aspects of gaming machine operation, the non-functional aspects of gaming machine operation comprising at least one of player visual display and attract animation, audio player feedback and attraction, real time video presentations, and commercial operating system;

wherein the gaming processing subsystem is physically separate from the general computing subsystem.

6. A gaming machine comprising:

a housing;

a user input connected to the housing;

a display connected to the housing; and

a control system at least in communication with the gaming machine, the control system comprising a processing platform that comprises:

a bus;

a gaming processing subsystem for controlling functional aspects of gaming machine operation coupled to the bus; and

a general computing subsystem for controlling non-functional aspects of gaming machine operation coupled to the bus;

wherein the gaming processing subsystem is physically separate from the general computing subsystem.

7. A gaming machine in accordance with claim 6 wherein the bus uses an interface protocol that consists of one of PCI, ISA, VME, and AGP.

8. A gaming machine in accordance with claim 6 wherein the aspects of gaming machine operation comprise at least one of game play history, game accounting, gaming machine access, I/O control, random number generation and game authentication algorithms.

9. A gaming machine in accordance with claim 6 wherein the non-functional aspects of gaming machine operation comprise at least one of player visual

display and attract animation, audio player feedback and attraction, real time video presentations, and commercial operating systems.

10. A gaming machine comprising:
a housing;
a user input connected to the housing;
a display connected to the housing; and
a control system at least in communication with the gaming machine, the control system comprising a processing platform that comprises:
a bus that uses an interface protocol consisting of one of PCI, ISA, VME, and AGP;
a gaming processing subsystem for controlling functional aspects of gaming machine operation coupled to the bus, the functional aspects of gaming machine operation comprising at least one of game play history, game accounting, gaming machine access, I/O control, random number generation and game authentication algorithms; and
a general computing subsystem for controlling non-functional aspects of gaming machine operation coupled to the bus, the non-functional aspects of gaming machine operation comprising at least one of player visual display and attract animation, audio player feedback and attraction, real time video presentations, and commercial operating system;
wherein the gaming processing subsystem is physically separate from the general computing subsystem.

PROCESSING PLATFORM FOR A GAMING MACHINE

ABSTRACT OF THE DISCLOSURE

A processing platform for operation of a gaming machine. The processing platform includes a gaming processing subsystem for controlling functional aspects of gaming machine operation and a general computing subsystem for controlling non-
5 functional aspects of gaming machine operation. The gaming processing subsystem and the general computing subsystem are physically separate from one another and are coupled to a bus included within the processing platform.

SF 1001345 v1

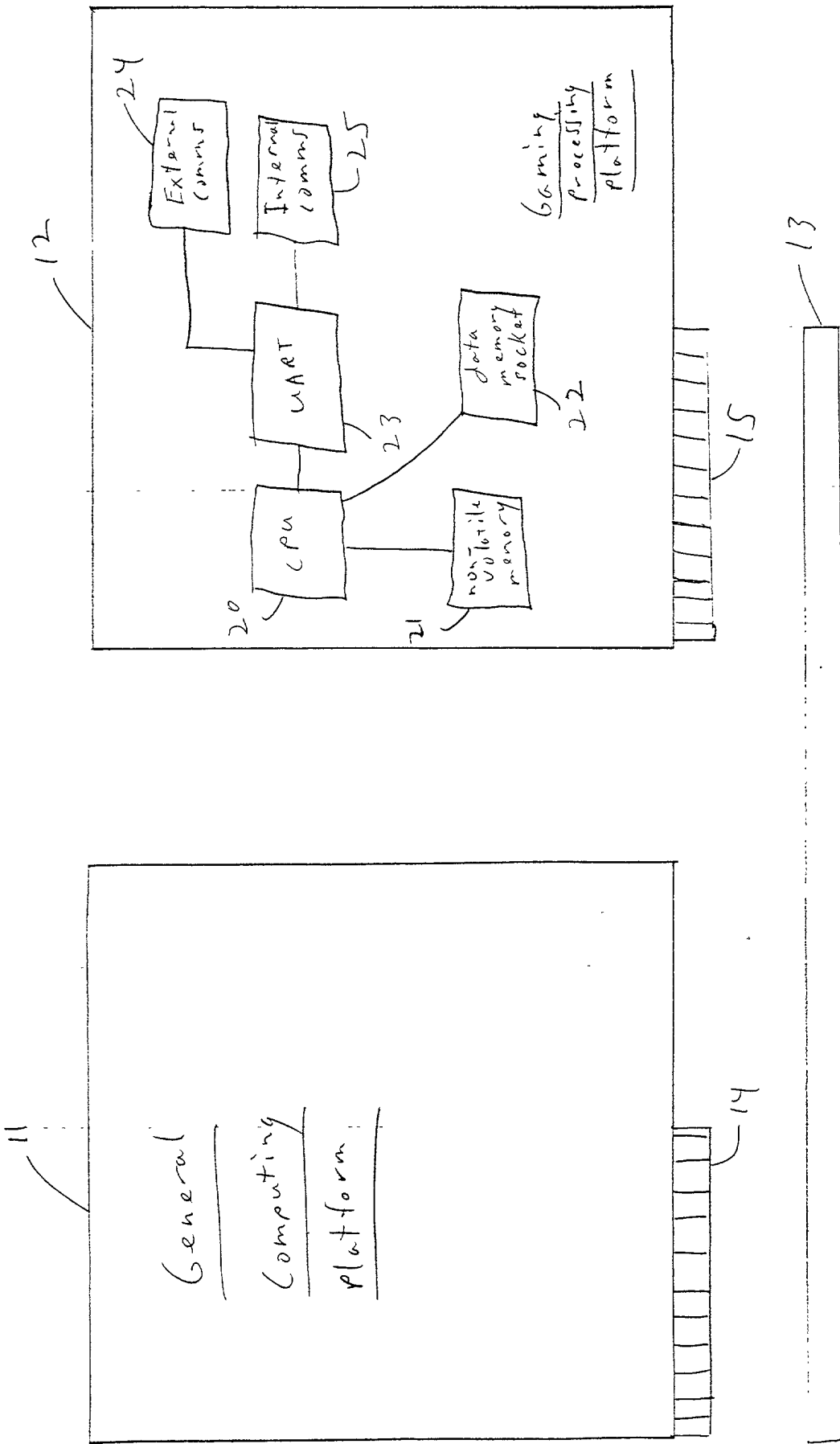


Figure 1

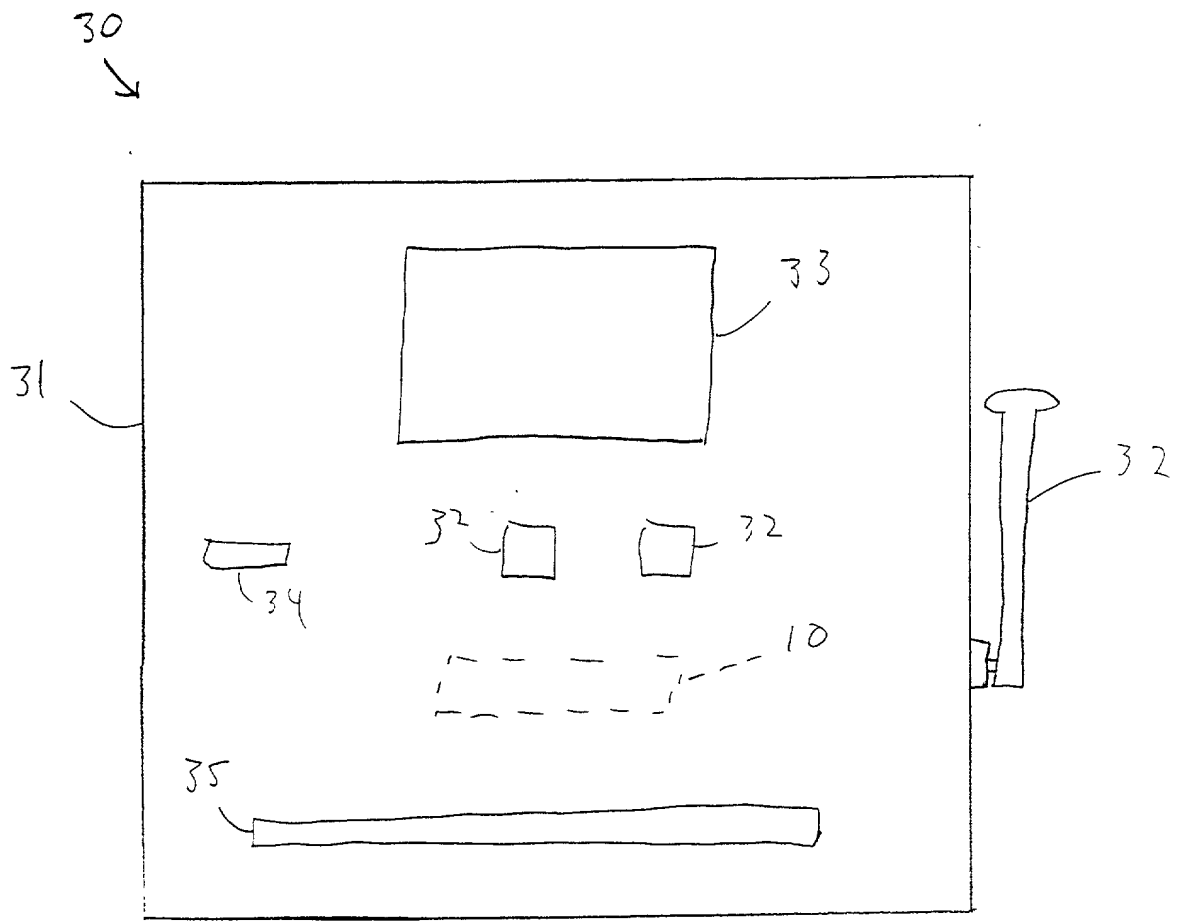


Figure 2

DECLARATION

As a below named inventor, I declare that:

My residence, post office address and citizenship are as stated below next to my name; I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural inventors are named below) of the subject matter which is claimed and for which a patent is sought on the invention entitled: **PROCESSING PLATFORM FOR A GAMING MACHINE** the specification of which X is attached hereto or _____ was filed on _____ as Application No. _____ and was amended on _____ (if applicable).

I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above. I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, Section 1.56. I claim foreign priority benefits under Title 35, United States Code, Section 119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed.

Prior Foreign Application(s)

Country	Application No.	Date of Filing	Priority Claimed Under 35 USC 119

I hereby claim the benefit under Title 35, United States Code § 119(e) of any United States provisional application(s) listed below:

Application No.	Filing Date

I claim the benefit under Title 35, United States Code, Section 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, Section 112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, Section 1.56 which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

Application No.	Date of Filing	Status

Full Name of Inventor 1:	Last Name: SNOW	First Name: RICHARD	Middle Name or Initial:	
Residence & Citizenship:	City: Reno	State/Foreign Country: NV	Country of Citizenship: United States of America	
Post Office Address:	Post Office Address: 4725 Travis Way	City: Reno	State/Country: NV	Postal Code: 89502
Full Name of Inventor 2:	Last Name: SHAFFER	First Name: STEVEN	Middle Name or Initial:	
Residence & Citizenship:	City: Reno	State/Foreign Country: NV	Country of Citizenship: United States of America	
Post Office Address:	Post Office Address: 15508 Quicksilver Drive	City: Reno	State/Country: NV	Postal Code: 89511

I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Signature of Inventor 1	Signature of Inventor 2
<hr/>	<hr/>
Richard Snow	Steven Shaffer
Date	Date